IN THE CLAIMS

1. (Currently amended) An electronic circuit ~~that includes~~comprising:

~~at least one~~a plurality of sequential logic elements ~~(12)~~ ~~that~~ each comprisinges:

at least one clock terminal for receiving a clock signal ~~(CLK)~~;

at least one input terminal ~~(D)~~ for receiving an input signal ~~(I)~~;

at least one output terminal ~~(Q)~~ for providing an output signal ~~(O)~~;

~~characterized in that said electronic circuit further comprises:~~ circuitry, for respective ones of the plurality of sequential logic elements, ~~(20)~~ for monitoring respective ones of said input and output signals ~~(I, O)~~ to provide respective a control signals ~~(CS)~~ in response ~~to said input and output signals~~thereto ~~(I, O)~~; and means for combining said respective control signals to form a combined control signal and controlling a power consumption of the electronic circuit in response to said combined control signal ~~(CS)~~.

2. (Currently amended) An electronic circuit as claimed in claim 1, characterized in that it is capable of being controlled at a rate determined by the clock signal ~~(CLK)~~.

3. (Previously presented) An electronic circuit as claimed in claim 1 characterized in that it is capable of providing information relating to future power consumption.

4. (Previously presented) An electronic circuit as claimed in claim 1, characterized by its ability of having future power consumption being controllable in advance based upon past logical events.

5. (Previously presented) An apparatus that includes an electronic circuit as claimed in claim 1.

6. (Currently amended) A method of controlling power consumption of an electronic circuit that includes ~~at least one~~a plurality of sequential logic elements ~~(12)~~ ~~that~~ each comprisinges: at least one clock terminal for receiving a clock signal ~~(CLK),~~; at least one

input terminal ~~(D)~~ for receiving an input signal ~~(I)~~,; and at least one output terminal ~~(Q)~~ for providing an output signal ~~(O); characterized in that~~, the method comprisinges the steps of:

for respective ones of the plurality of sequential logic elements, monitoring respective ones of said input and output signals to provide respective control signals in response thereto; and

combining said respective control signals to form a combined control signal and controlling a power consumption of the electronic circuit in response to said combined control signal.~~monitoring said input and output signals (I, O); providing a control signal (CS) in response to the input and output signals (I, O); and operatively controlling the power consumption in response to the control signal.~~